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By: 

Date: June 4, 2003

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6-11-03

9. Inter

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Lothar Risch et al.

Applic. No. : 09/996,279

Filed : November 28, 2001

Title : Double Gate MOSFET Transistor and Method for the Production Thereof

Examiner : Shrinivas H. Rao

Group Art Unit : 2814

enter
Marsha
7/9/03

AMENDMENT UNDER 37 C.F.R. § 1.116

Hon. Commissioner of Patents and Trademarks,

Sir :

Responsive to the final Office Action dated March 4, 2003,
kindly amend the above-identified application as follows:

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